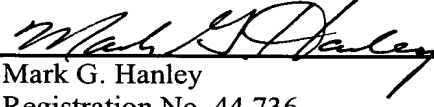




PATENT  
Docket No. Intel/18495

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

Applicants: Long et al.	)	I hereby certify that this paper is
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For: Methods and Apparatus for Merging	)	Box 1450, Alexandria, VA 22313-
Critical Sections	)	1450 on this date:
	)	
Group Art Unit: 2186	)	<b>July 12, 2004</b>
	)	
Examiner: Unknown	)	
	)	Mark G. Hanley
	)	Registration No. 44,736
	)	Attorney for Applicant(s)

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
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Sir:

The document listed on the enclosed PTO Form-1449 is submitted pursuant to 37  
CFR §§ 1.56, 1.97, and 1.98. A copy of the document is enclosed.

**TIME OF FILING**

This information disclosure statement is being filed, to the best of the  
undersigned's knowledge, before the mailing date of a first Office action on the merits.

## METHOD OF PAYMENT

No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

Correspondence Address:

Respectfully submitted,

GROSSMAN & FLIGHT, LLC.  
20 N. Wacker Drive  
Suite 4220  
Chicago, Illinois 60606  
(312) 580-1020

By:



Mark G. Hanley

Registration No.: 44,736

**July 12, 2004**

Attorney for Intel Corporation



SHEET 1 of 1

Form PTO-1449 (Modified)

JUL 16 2004

U.S. Department of Commerce  
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INTEL/18495

Serial No.

10/804,735

Applicant

Li et al.

Filing Date

03/19//2004

Group Art Unit

2186

## INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

C01	Li et al., U.S. Patent Application Serial No. 10/714,198, Filed November 14, 2003, " <i>An Apparatus and Method for an Automatic Thread-Partition Compiler</i> ", 42 pages
C02	

EXAMINER

DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.